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| FORM PTO 1449 ( <i>modified</i> )<br><br>U.S. DEPARTMENT OF COMMERCE<br>PATENT AND TRADEMARK OFFICE<br><br>LIST OF REFERENCES CITED BY APPLICANT(S)<br>(Use several sheets if necessary) |  |  |  | ATTY DOCKET NO.<br><b>US020465US</b>         |  | APPLICATION NO.<br><b>10/535,591</b> |  |
|  |  |  |  | APPLICANT<br><b>Jan-Willem Van De Waerdt</b> |  |                                      |  |
|  |  |  |  | FILING DATE<br><b>May 19, 2005</b>           |  | GROUP<br><b>2187</b>                 |  |

  

| U.S. PATENT DOCUMENTS |  |                    |      |      |       |          |                               |
|-----------------------|--|--------------------|------|------|-------|----------|-------------------------------|
| EXAMINER<br>INITIAL   |  | DOCUMENT<br>NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE<br>IF APPROPRIATE |
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| FOREIGN PATENT DOCUMENTS |  |                    |      |         |       |          |                                       |
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|                          |  | DOCUMENT<br>NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION<br>YES/NO/<br>OR ABSTRACT |
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| OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.) |  |  |
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| /G.C./   |  | <b>Kim, S. et al. "Stride-Directed Prefetching for Secondary Caches", Proc. 1997 Int'l. Conf. on Parallel Processing, ps. 314-21 (8/1997)</b>  |
| /G.C./   |  | <b>Hariprakash, G. et al. "DSTRIDE: Data-Cache Miss-Address-Based Stride Prefetching Scheme For Multimedia Processors", Proc. 6th Australasian Computer Sys. Architecture Conf., pgs. 62-70 (1/2001)</b> |
| /G.C./   |  | <b>Vanderwiell, S. et al. "Data Prefetch Mechanisms", ACM Computing Surveys, vol. 32, no. 2, pgs. 174-99 (6/2000)</b>  |
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| EXAMINER<br>/Gary Cygiel/ | DATE CONSIDERED<br>08/18/2010 |
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.